



P30449.A03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Masato MORI et al. Group Art Unit: 1793
Appln. No. : 10/597,949 Examiner: PATEL, DEVANG R
(U.S. National Stage of PCT/JP2005/003043)
I.A. Filed : February 24, 2005 Confirmation No. : 8311
For : ELECTRONIC COMPONENT MOUNTING METHOD, AND CIRCUIT
SUBSTRATE AND CIRCUIT SUBSTRATE UNIT USED IN THE METHOD

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and §§1.97-1.98, and supplemental to the Information Disclosure Statement that was previously filed on November 14, 2006, Applicants hereby bring to the attention of the Examiner a Chinese Office Action dated August 29, 2008, with respect to patent family member Chinese Patent Application No. 200580005582.1, in which the following documents were cited:

- /D.P./ (1) Japanese Patent Publication No. 2001-239395 A; and
/D.P./ (2) Japanese Patent Publication No. 2002-343828 A.

The relevance of these documents, as ascertained with respect to the Chinese claims by the Chinese Examiner, is set forth in the Chinese Office Action.

Applicants previously submitted above-noted documents (1) and (2) in the Information Disclosure Statement that was filed on November 14, 2006.

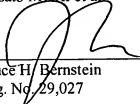
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P30449.A03

Copies of the Chinese Office Action and an English translation thereof are attached hereto.

Should the Examiner have any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
Masato MORI et al.



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January 29, 2009
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